

10/24/48
09/04/48
U.S. PRO

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

Jc3
866

<u>SA</u>	O.I.P.E.	PATENT DATE
SCANNED	TR 2 Q.A. <u>JM</u>	

APPLICATION NO. 09/904748	CONT/PRIOR	CLASS 707 <i>710</i>	SUBCLASS 22	ART UNIT 2171 <i>2196</i>	EXAMINER <i>Shin</i>
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APPlicants

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Stephan Novak

710

BCLASS
22

ART U

PATENT DATE

Method and apparatus for accessing memories having a time-variant response over a PCI bus by using two-stage DMA transfers

TITLE

PTO-2040
12/02

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig. /	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed				Amount Due	Date Paid
ISSUE BATCH NUMBER					

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